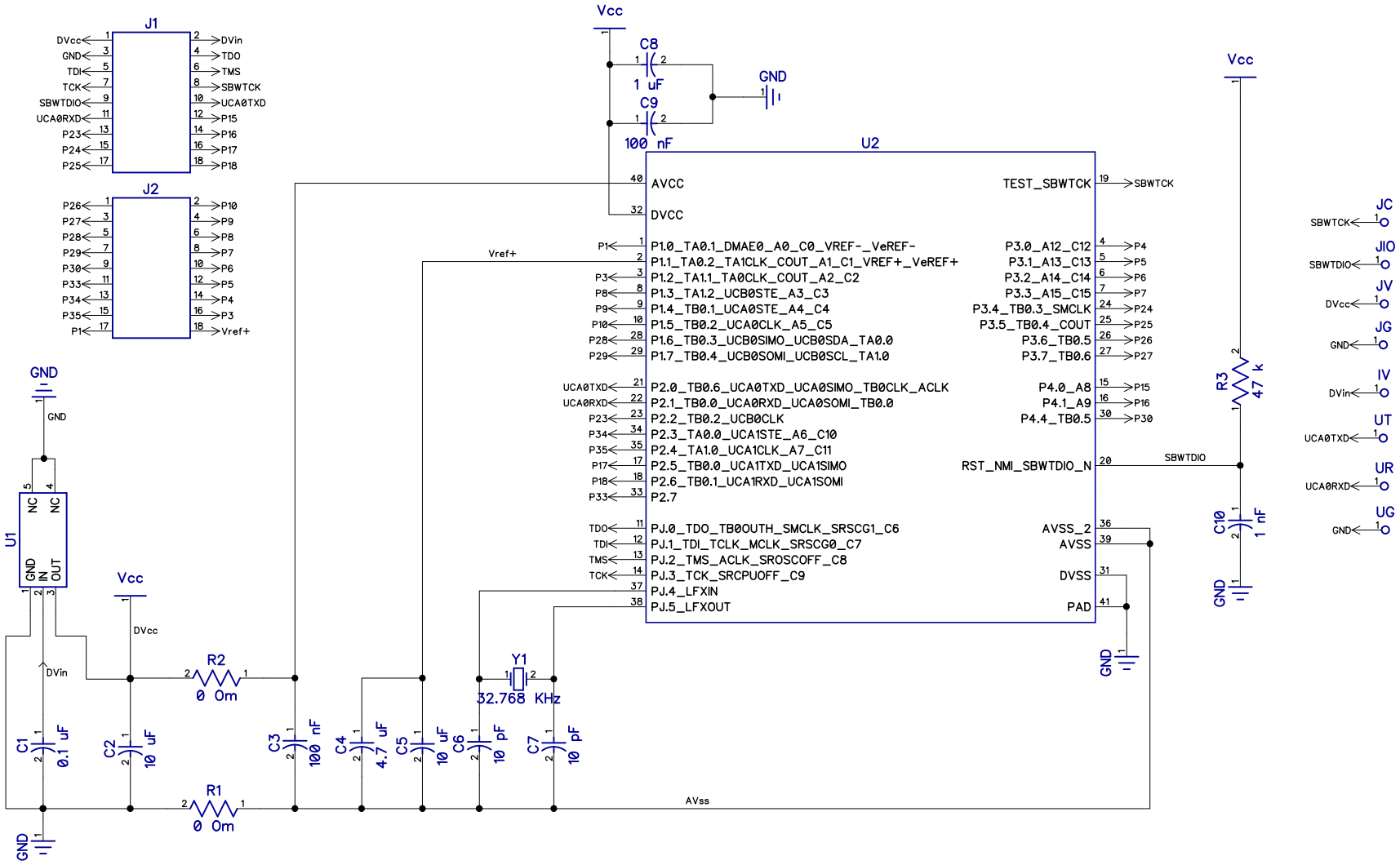
J1
DVcc 1
GND 3
TDI 5
TCK 7
SBWTDIO 9
UCA0RXD 11
P23 13
P24 15
P25 17
2 DVin
4 TDO
6 TMS
8 SBWTCK
10 UCA0TXD
12 P15
14 P16
16 P17
18 P18
J2
P26 1
P27 3
P28 5
P29 7
P30 9
P33 11
P34 13
P35 15
P1 17
2 P10
4 P9
6 P8
8 P7
10 P6
12 P5
14 P4
16 P3
18 Vref+
Vcc
C8
1 uF
C9
100 nF
GND
U2
AVCC 40
DVCC 32
P1 1 P1.0_TA0.1_DMAE0_A0_C0_VREF-_VeREF-
2 P1.1_TA0.2_TA1CLK_COUT_A1_C1_VREF+_VeREF+
P3 3 P1.2_TA1.1_TA0CLK_COUT_A2_C2
P8 8 P1.3_TA1.2_UCB0STE_A3_C3
P9 9 P1.4_TB0.1_UCA0STE_A4_C4
P10 10 P1.5_TB0.2_UCA0CLK_A5_C5
P28 28 P1.6_TB0.3_UCB0SIMO_UCB0SDA_TA0.0
P29 29 P1.7_TB0.4_UCB0SOMI_UCB0SCL_TA1.0
UCA0TXD 21 P2.0_TB0.6_UCA0TXD_UCA0SIMO_TB0CLK_ACLK
UCA0RXD 22 P2.1_TB0.0_UCA0RXD_UCA0SOMI_TB0.0
P23 23 P2.2_TB0.2_UCB0CLK
P34 34 P2.3_TA0.0_UCA1STE_A6_C10
P35 35 P2.4_TA1.0_UCA1CLK_A7_C11
P17 17 P2.5_TB0.0_UCA1TXD_UCA1SIMO
P18 18 P2.6_TB0.1_UCA1RXD_UCA1SOMI
P33 33 P2.7
TDO 11 PJ.0_TDO_TB0OUTH_SMCLK_SRSCG1_C6
TDI 12 PJ.1_TDI_TCLK_MCLK_SRSCG0_C7
TMS 13 PJ.2_TMS_ACLK_SROSCOFF_C8
TCK 14 PJ.3_TCK_SRCPUOFF_C9
37 PJ.4_LFXIN
38 PJ.5_LFXOUT
TEST_SBWTCK 19 SBWTCK
P3.0_A12_C12 4 P4
P3.1_A13_C13 5 P5
P3.2_A14_C14 6 P6
P3.3_A15_C15 7 P7
P3.4_TB0.3_SMCLK 24 P24
P3.5_TB0.4_COUT 25 P25
P3.6_TB0.5 26 P26
P3.7_TB0.6 27 P27
P4.0_A8 15 P15
P4.1_A9 16 P16
P4.4_TB0.5 30 P30
RST_NMI_SBWTDIO_N 20 SBWTDIO
AVSS_2 36
AVSS 39
DVSS 31
PAD 41
Vref+
Vcc
R3
47 k
C10
1 nF
GND
JC SBWTCK
JIO SBWTDIO
JV DVcc
JG GND
IV DVin
UT UCA0TXD
UR UCA0RXD
UG GND
U1
NC
NC
GND
IN
OUT
DVin
DVcc
R2
0 Om
Y1
32.768 KHz
C1 0.1 uF
C2 10 uF
C3 100 nF
C4 4.7 uF
C5 10 uF
C6 10 pF
C7 10 pF
R1
0 Om
AVss